

PATENT
Docket No. 402912000100

DECLARATION FOR UTILITY PATENT APPLICATION

AS BELOW-NAMED INVENTORS, WE HEREBY DECLARE THAT:

Our residence, post office address, and citizenships are as stated below next to our names.

We believe we are the original, first and joint inventors of the subject matter which is claimed and for which a patent is sought on the invention entitled: MEMORY MANAGEMENT SYSTEM AND ALGORITHM FOR NETWORK PROCESSOR ARCHITECTURE, the nonprovisional patent application for this invention provided by:

(1) the Provisional Patent Application filed on October 23, 2000 and assigned application number 60/242,831, and

(2) the Preliminary Amendment filed herewith.

WE HEREBY STATE THAT WE HAVE REVIEWED AND UNDERSTAND THE CONTENTS OF THE ABOVE-IDENTIFIED APPLICATION AND PRELIMINARY AMENDMENT.

We acknowledge the duty to disclose information which is material to the patentability as defined in 37 C.F.R. § 1.56.

We hereby claim foreign priority benefits under 35 U.S.C. § 119(a)-(d) or § 365(b) of any foreign application(s) for patent or inventor's certificate, or § 365(a) of any PCT International application which designated at least one country other than the United States listed below and have also identified below, by checking the box, any foreign application for patent or inventor's certificate, or PCT International application having a filing date before that of the application on which priority is claimed:

Application No.	Country	Date of Filing (day/month/year)	Priority Claimed?
			<input type="checkbox"/> Yes <input type="checkbox"/> No

We hereby claim benefit under 35 U.S.C. § 119(e) of any United States provisional application(s) listed below:

Application Serial No.	Filing Date

We hereby claim the benefit under 35 U.S.C. § 120 of any United States application(s), or § 365(c) of any PCT International application designating the United States, listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States or PCT International application in the manner provided by the first paragraph of 35 U.S.C. § 112, we acknowledge the duty to disclose information which is material to

patentability as defined in 37 C.F.R. § 1.56 which became available between the filing date of the prior application and the national or PCT International filing date of this application.

Application Serial No.	Filing Date	Status
		<input type="checkbox"/> Patented <input type="checkbox"/> Pending <input type="checkbox"/> Abandoned

We hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under § 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

10/22/01

Date



Name: Ryszard BLESZYNSKI

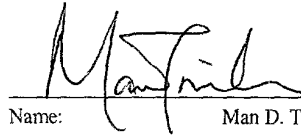
Residence: 18817 Bellgrove Circle, Saratoga, California 95070

Citizenship: Indonesia

Post Office Address: 18817 Bellgrove Circle, Saratoga, California 95070

10/22/01

Date



Name: Man D. TRINH

Residence: 2215 Emerald Hills Drive, San Jose, California 95131

Citizenship: United States of America

Post Office Address: 2215 Emerald Hills Drive, San Jose, California 95131



PATENT
Docket No. 402912000100

In the application of:

Ryszard BLESZYNSKI and
Man Dieu TRINH

Serial No.: To Be Assigned

Filing Date: Herewith

For: MEMORY MANAGEMENT SYSTEM
AND ALGORITHM FOR NETWORK
PROCESSOR ARCHITECTURE

Examiner: To Be Assigned

Group Art Unit: To Be Assigned

**PROSECUTION BY ASSIGNEE AND POWER OF ATTORNEY
UNDER 37 C.F.R. § 3.71**

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

BAY MICROSYSTEMS, INC., the assignee of the entire right, title and interest in this patent application, under 37 C.F.R. § 3.71 hereby appoints:

Lisa A. Amii (Reg No. 48,199)
Mehran Arjomand (Reg No. 48,231)
Sanjay S. Bagade (Reg No. 42,280)
Shantanu Basu (Reg No. 43,318)
Vincent J. Belusko (Reg No. 30,820)
Kimberly A. Bolin (Reg No. 44,546)
Tyler S. Brown (Reg No. 36,465)
A. Randall Camacho (Reg No. 46,595)
Robert K. Cerpa (Reg No. 39,933)
Alex Chartove (Reg No. 31,942)
Thomas E. Ciotti (Reg No. 21,013)
Matthew M. D'Amore (Reg No. 42,457)
Peter Davis (Reg No. 36,119)
Stephen C. Durant (Reg No. 31,506)
David L. Fehrman (Reg No. 28,600)
Thomas George (Reg No. 45,740)

Randolph Ted Apple (Reg No. 36,429)
Laurie A. Axford (Reg No. 35,053)
Erwin J. Basinski (Reg No. 34,773)
Richard R. Batt (Reg No. 43,485)
Jonathan Bockman (Reg No. 45,640)
Barry E. Bretschneider (Reg No. 28,055)
Nicholas Buffinger (Reg No. 39,124)
Mark R. Carter (Reg No. 39,131)
Peng Chen (Reg No. 43,543)
Thomas Chuang (Reg No. 44,616)
Cara M. Coburn (Reg No. 46,631)
Raj S. Davé (Reg No. 42,465)
Karen B. Dow (Reg No. 29,684)
Carolyn A. Favorito (Reg No. 39,183)
Hector Gallegos (Reg No. 40,614)
Debra J. Glaister (Reg No. 33,888)

Kenneth R. Glick (Reg No. 28,612)
John U. Han (Reg No. 45,565)
Alan S. Hodes (Reg No. 38,185)
Kelvan P. Howard (Reg No. P48,999)
Jill A. Jacobson (Reg No. 40,030)
Madeline I. Johnston (Reg No. 36,174)
Ararat Kapouytian (Reg No. 40,044)
Cameron A. King (Reg No. 41,897)
Kawai Lau (Reg No. 44,461)
Rimas T. Lukas (Reg No. 46,451)
Gladys H. Monroy (Reg No. 32,430)
Kate H. Murashige (Reg No. 29,959)
Martin M. Noonan (Reg No. 44,264)
Phillip Reilly (Reg No. 41,415)
Debra A. Shetka (Reg No. 33,309)
Rebecca Shortle (Reg No. 47,083)
Stanley H. Thompson (Reg No. 45,160)
Brenda J. Wallach (Reg No. 45,193)
E. Thomas Wheelock (Reg No. 28,825)
Frank Wu (Reg No. 41,386)
Peter J. Yim (Reg No. 44,417)
Karen R. Zachow (Reg No. 46,332)

Bruce D. Grant (Reg No. 47,608)
Douglas G. Hodder (Reg No. 41,840)
Charles D. Holland (Reg No. 35,196)
Peter Hsieh (Reg No. 44,780)
Wayne Jaeschke, Jr. (Reg No. 38,503)
Parisa Jorjani (Reg No. 46,813)
Richard C. Kim (Reg No. 40,046)
Lawrence B. Kong (Reg No. P49,043)
Glenn Kubota (Reg No. 44,197)
Michael J. Mauriel (Reg No. 44,226)
Philip A. Morin (Reg No. P-45,926)
Mabel Ng (Reg No. P48,922)
Catherine M. Polizzi (Reg No. 40,130)
Robert E. Scheid (Reg. No. 42,126)
Terri Shieh-Newton (Reg No. 47,081)
Kevin R. Spivak (Reg No. 43,148)
Thomas L. Treffert (Reg No. P48,279)
Michael R. Ward (Reg No. 38,651)
Todd W. Wight (Reg No. 45,218)
David T. Yang (Reg No. 44,415)
George C. Yu (Reg No. 44,418)

all of Morrison & Foerster LLP, 755 Page Mill Road, Palo Alto, California 94304-1018, telephone (650) 813-5600, to prosecute this application and transact all matters in the United States Patent and Trademark Office connected therewith, said appointment to be to the exclusion of the inventors and their attorneys in accordance with the provisions of 37 C.F.R. § 3.71 provided that if any one of said attorneys or agents ceases being affiliated with the law firm of Morrison & Foerster as partner, employee or of counsel, such attorney's or agent's appointment as attorney or agent and all powers derived therefrom shall terminate on the date such attorney or agent ceases being so affiliated.

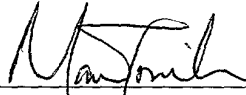
Please direct all written communications relative to this application to:

Thomas George
Morrison & Foerster LLP
755 Page Mill Road
Palo Alto, California 94304-1018

Please direct all telephone communications to Thomas George at (650) 813-5722.

BAY MICROSYSTEMS, INC.
a Delaware corporation

Dated: October 22 2001



Name: MAN PRINS
Title: DIR. SYSTEMS ENGR/ARCHITECTURE
Address: 2700 Augustine Drive, Suite 298
Santa Clara, CA 95054-2911

Applicant/Patentee: Ryszard BLESZYNSKI and Man Dieu TRINH

Docket No.: 402912000100

Serial No./Patent No.: To Be Assigned

Filed on/Issued : Herewith

For: MEMORY MANAGEMENT SYSTEM AND ALGORITHM FOR NETWORK PROCESSOR ARCHITECTURE

VERIFIED STATEMENT CLAIMING SMALL ENTITY STATUS
37 C.F.R. §§ 1.9(f) AND 1.27(c) — SMALL BUSINESS CONCERN

I hereby declare that I am

- ☐ the owner of the small business concern identified below:
☒ an official of the small business concern empowered to act on behalf of the concern identified below:

NAME OF CONCERN: BAY MICROSYSTEMS, INC.

ADDRESS OF CONCERN: 2700 Augustine Drive, Suite 298, Santa Clara, California 95054

I hereby declare that the above identified small business concern qualifies as a small business concern as defined in 13 C.F.R. § 121.12, and reproduced in 37 C.F.R. § 1.9(d), for purposes of paying reduced fees to the United States Patent and Trademark Office, in that the number of employees of the concern, including those of its affiliates, does not exceed 500 persons. For purposes of this statement, (1) the number of employees of the business concern is the average over the previous fiscal year of the concern of the persons employed on a full-time, part-time or temporary basis during each of the pay periods of the fiscal year, and (2) concerns are affiliates of each other when either, directly or indirectly, one concern controls or has the power to control the other, or a third party or parties controls or has the power to control both.

I hereby declare that rights under contract or law have been conveyed to and remain with the small business concern identified above with regard to the invention, entitled MEMORY MANAGEMENT SYSTEM AND ALGORITHM FOR NETWORK PROCESSOR ARCHITECTURE

- ☐ the specification filed herewith with title as listed above.
☒ the application identified above.
☐ the patent identified above.

If the rights held by the above identified business concern are not exclusive, each individual, concern or organization having rights in the invention must file separate verified statements averring to their status as small entities, and no rights to the invention are held by any person, other than the inventor, who would not qualify as an independent inventor under 37 C.F.R. § 1.9(c) if that person made the invention, or by any concern which would not qualify as a small business concern under 37 C.F.R. § 1.9(d), or a nonprofit organization under 37 C.F.R. § 1.9(e).

Each person, concern or organization having any rights in the invention is listed below:

- ☒ no such person, concern, or organization exists.
☐ each such person, concern or organization is listed below.

NAME	ADDRESS	TYPE
		<input type="checkbox"/> Individual <input type="checkbox"/> Small Business Concern <input type="checkbox"/> Nonprofit Organization

Separate verified statements are required from each named person, concern or organization having rights to the invention averring to their status as small entities. (37 C.F.R. § 1.27)

I acknowledge the duty to file, in this application or patent, notification or any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate. (37 C.F.R. § 1.28(b))

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this verified statement is directed.

NAME OF PERSON SIGNING:

TITLE OF PERSON IF OTHER THAN OWNER:

ADDRESS OF PERSON SIGNING:

MAN TRINH
DIRECTOR, SYSTEMS ENGR/ARCHITECTURE
2700 Augustine Drive, Suite 298, Santa Clara, California 95054

SIGNATURE: 

DATE: 10/22/07